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L6	4	(714/741.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block\$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 19:41
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L8	6	(714/726.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block\$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 19:41
L9	0	(714/717.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block\$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/13 19:41



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